

High-speed visual target tracking with mixed rotation invariant description and skipping searching

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Abstract

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1 Introduction the most fundamental technologies in computer

Target tracking is one of the most fundamental technologies in computer vision. Many tracking systems have been designed for various applications such as industrial automation, autonomous robot navigation, intelligent video surveillance, activity analysis, classification and recognition from motion, and human-computer interfaces [1, 2]. However, (??) the respond speed of conventional tracking system is limited to less than 60 fps due to serial image processing. Some researchers adopt parallel single-instruction-multiple-data (SIMD) processors to speed up tracking algorithms [3–7]. However, these processors are Sect. 1 still unable to meet the needs of high-speed target tracking applications because they can only implement straightforward low-level and some simple middle-level image processing <https://mc03.manuscriptcentral.com/scpma> algorithms such as background subtraction, segmentation and motion detection.

In many applications such as robot navigation and auto-

matic drive, the target moves at high speed in complex environment. In order to tracking a moving target constantly, not only real-time processing capacity but also robust algorithms are required. Image processing algorithms such as

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optical flow, self-window and moment tracking have been implemented on high-speed vision systems to realize fast tracking functions [?, 8, 9]. However, most of them can only be applied to certain scenarios with a clear background or constant illumination. Some other algorithms such as compressive tracking and tracking-learning-detection have better performance in robustness; however, their computation overhead constrains their real-time performance [11, 12]. Local binary pattern (LBP)-histogram of gradient (HOG) feature description is widely used in target detection and tracking [9, 10]. However, both of the HOG and LBP histogram are rotation variant. Hence, it would result in target shifting and tracking failing when the target moves in a complicated environment or rotates suddenly.

Figure 1 Process of the proposed MRID.**Table 1** FPGA resource utilization

FPGA device	ArriaV 5AGXFB3H4F35C4N
Logic utilization (in ALMs)	83845/136880 (61%)
Total registers	83534
Total pins	41/656 (6%)
Total block memory bits	2350280/17674240 (13%)
Total PLLs	2/36 (6%) ¹⁾

¹⁾ spacecraft will hit earth on way back since $r_p < r_{earth}$

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Appendix